

TRANSMITTAL

Electronic Version v1.1

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Title of Invention	METHOD OF FORMING FET SILICIDE GATE STRUCTURES INCORPORATING INNER SPACERS	
Application Number :		
Date :		
First Named Applicant:	Victor Ku	
Confirmation Number:		
Attorney Docket Number:	FIS920030341	
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Submitted By:		Elec. Sign.
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		Sign. Capacity Attorney

Documents being submitted:	Files
us-assignment	FIS920030341-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030341assign1.tif FIS920030341assign2.tif
us-request	FIS920030341-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FIS920030341-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	FIS920030341-usidst.xml us-ids.dtd us-ids.xsl
us-declaration	FIS920030341dec1.tif
us-declaration	FIS920030341dec2.tif
us-declaration	FIS920030341dec3.tif
us-declaration	FIS920030341dec4.tif
application-body	FIS920030341-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd FIS920030341dwg1.tif FIS920030341dwg2.tif FIS920030341dwg3.tif

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Comments

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